

TITLE: Input Buffer Circuit INVENTORS: Kyu-Nam Lir Attorney Docket No.: AB-984-1

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FIG. 1 (PRIOR ART)

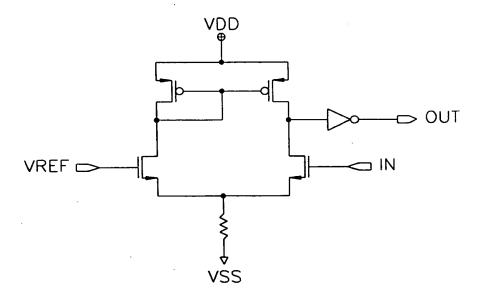


FIG. 2 (PRIOR ART)

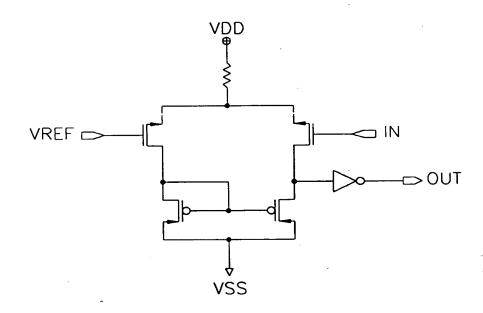
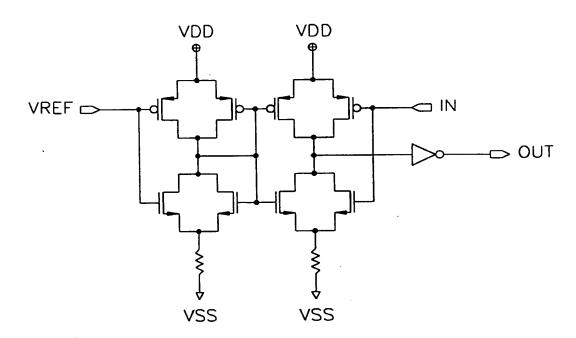


FIG. 3 (PRIOR ART)



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7 02 P44 45 P45 N43 P43 VDD VSS N2 Z N46 P46 P41 P42 N41 N42 0



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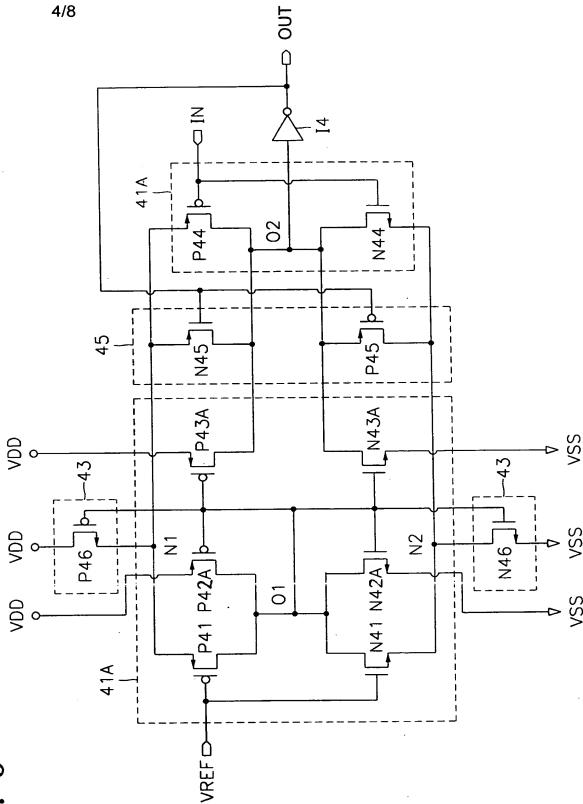
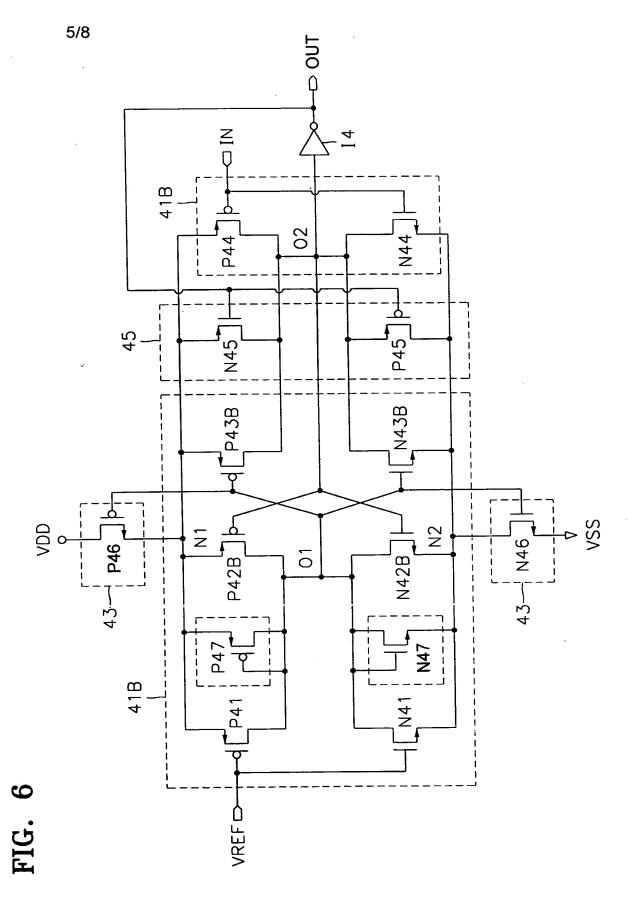


FIG. 5



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FIG. 7A

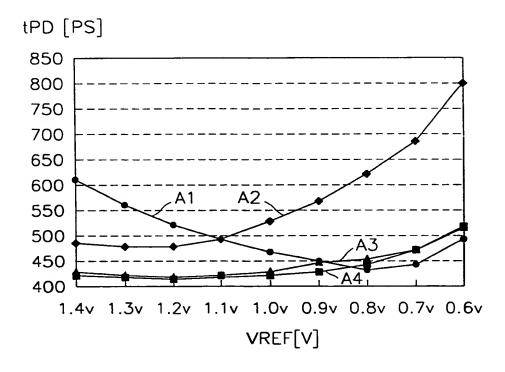
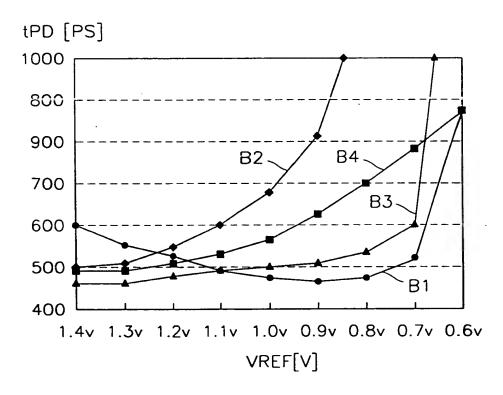


FIG. 7B





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FIG. 8A

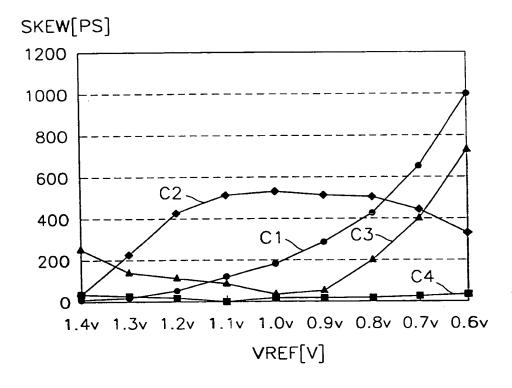


FIG. 8B

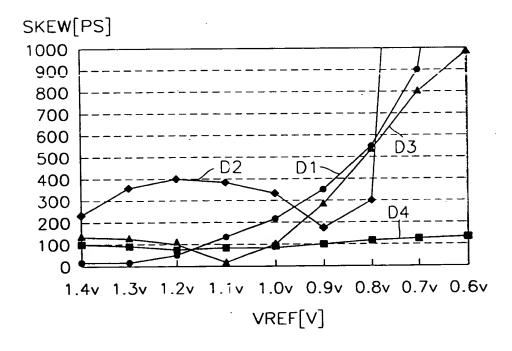


FIG. 9

